

# EB306

R1.03

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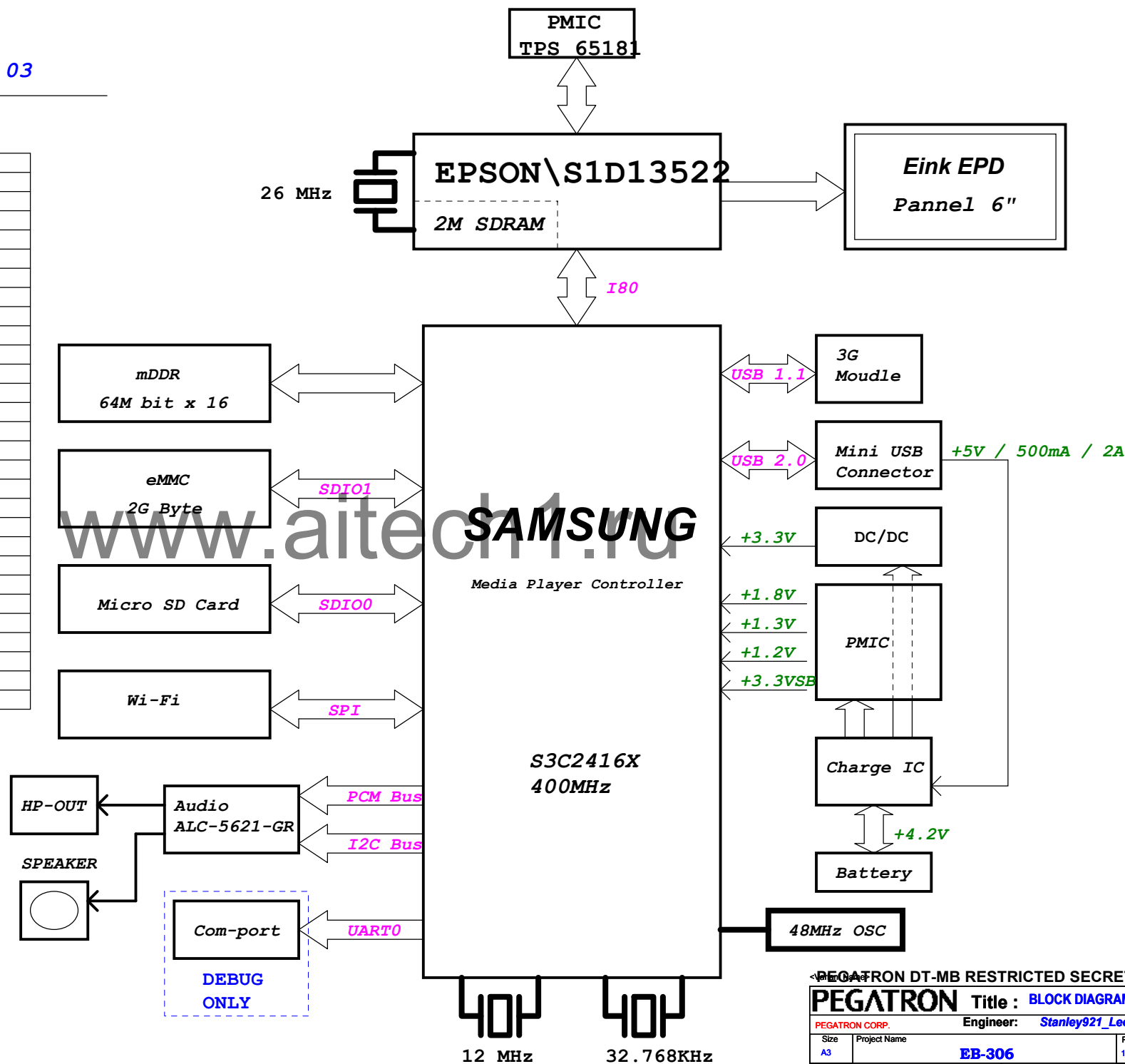
## CAD Note:

Default component footprint is SMD 0402 type. Difference footprint show on schematics.

I = Installed Part.

NI = Not Installed Part.

PROTO = PROTO Phase Only.



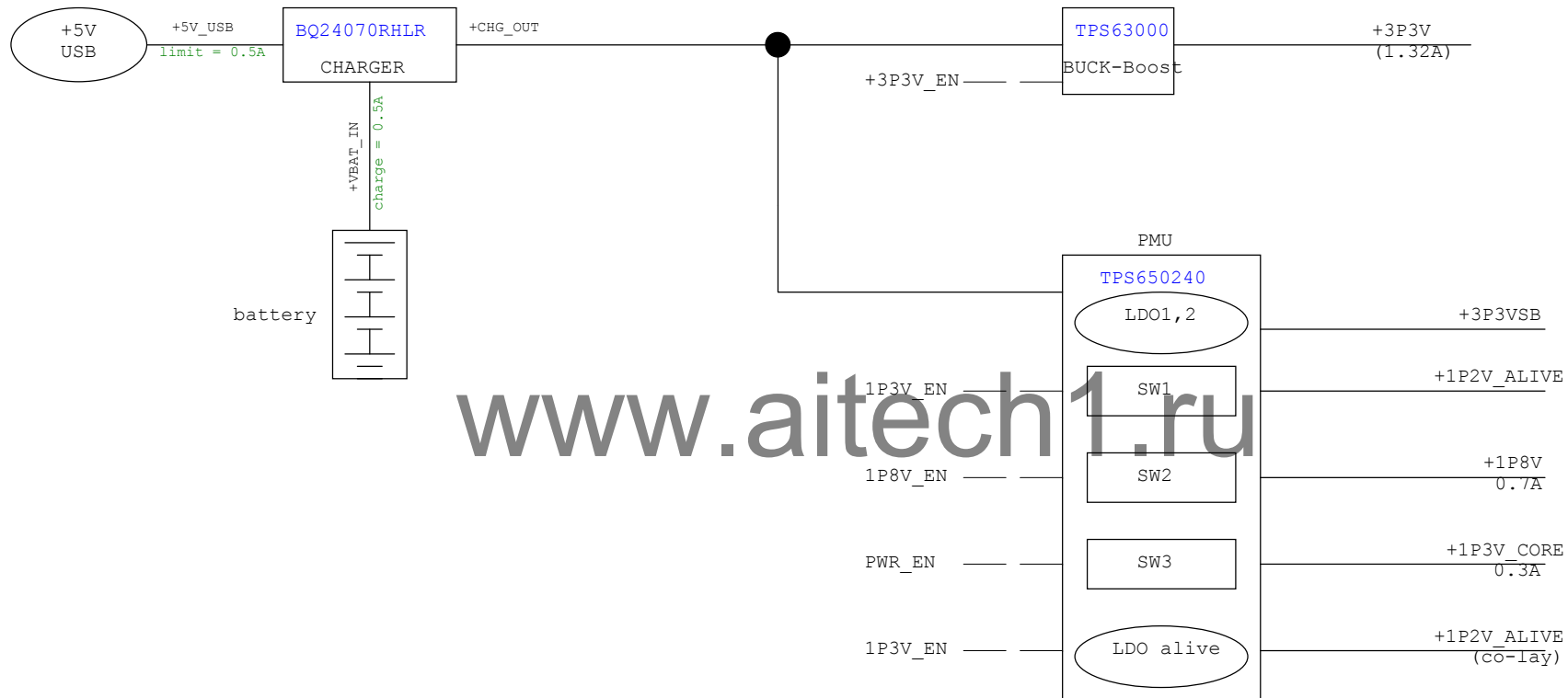
PEGATRON DT-MB RESTRICTED SECRET

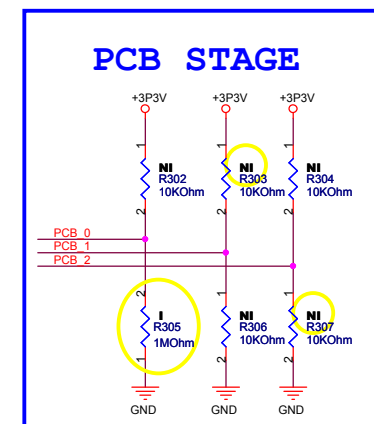
**PEGATRON** Title: **BLOCK DIAGRAM**

PEGATRON CORP. Engineer: **Stanley921\_Lee**

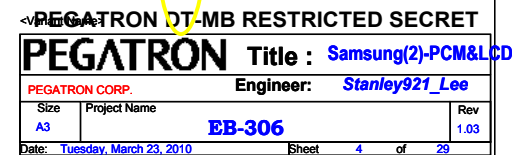
Size A3 Project Name **EB-306** Rev 1.03

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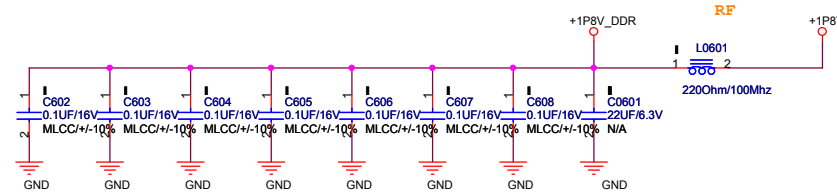


**FOOT PRINT:**  
**nb\_bga\_330p\_26\_551x551**

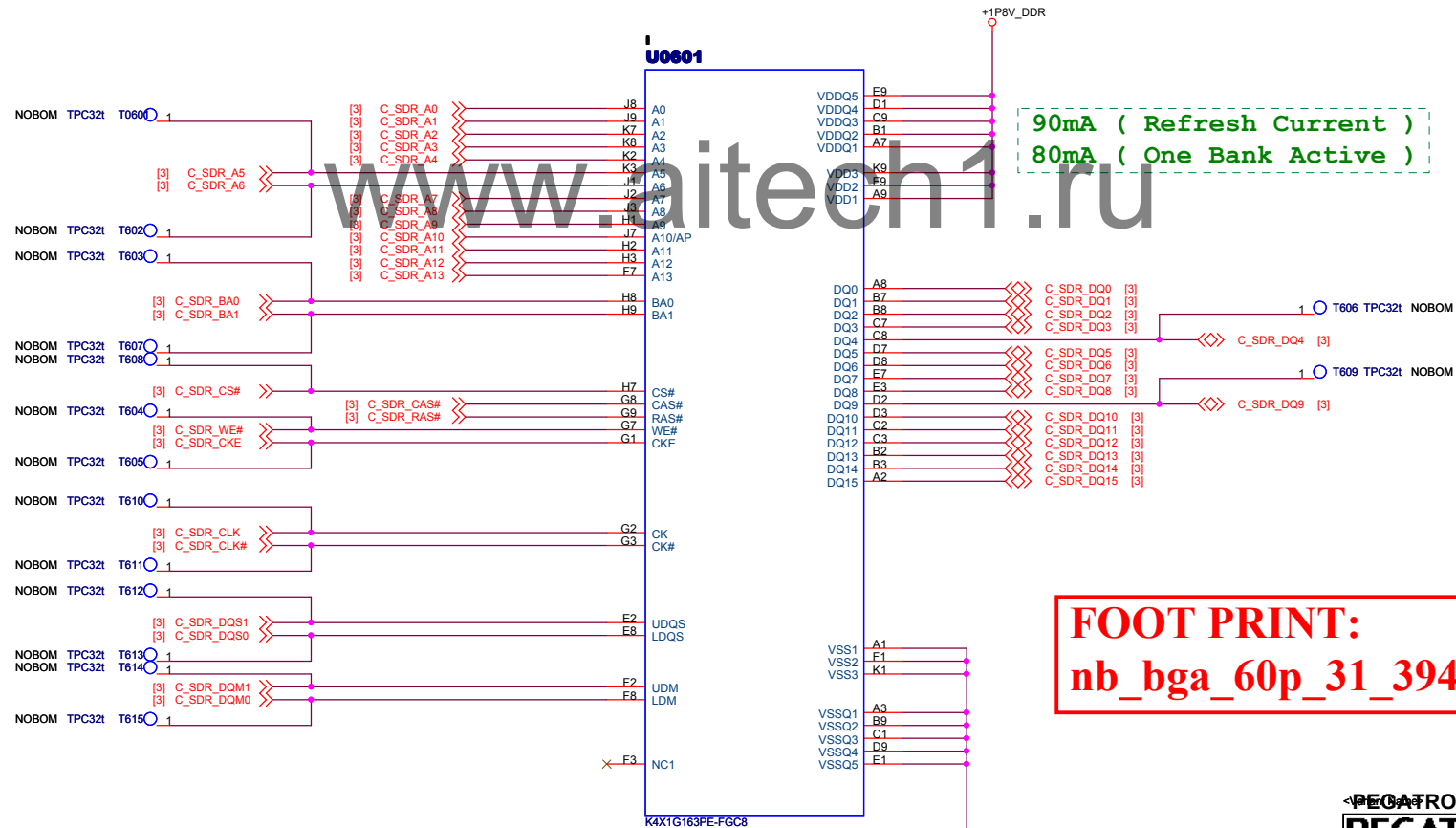




# +1P8V\_DDR



# mDDR

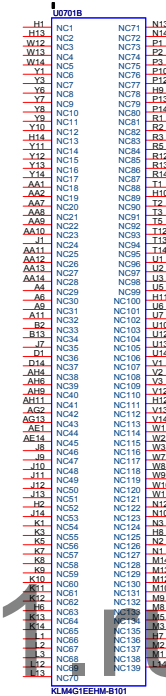


**FOOT PRINT:**  
nb\_bga\_60p\_31\_394x315

PEGATRON DT-MB RESTRICTED SECRET

<b>PEGATRON</b>		Title :	mDDR-128MB	
PEGATRON CORP.		Engineer:	Stanley921_Lee	
Size	Project Name			Rev
A3	EB-306			1.03
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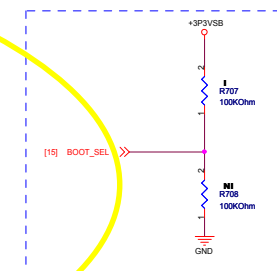
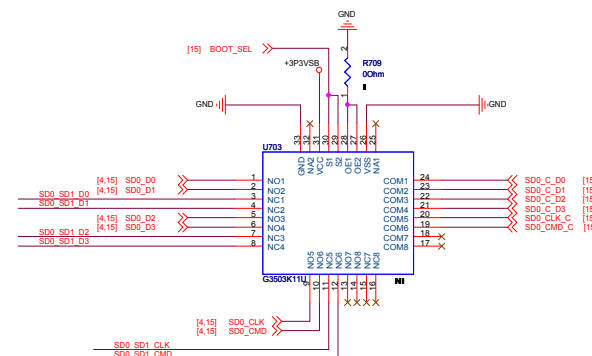
## moviNAND Flash



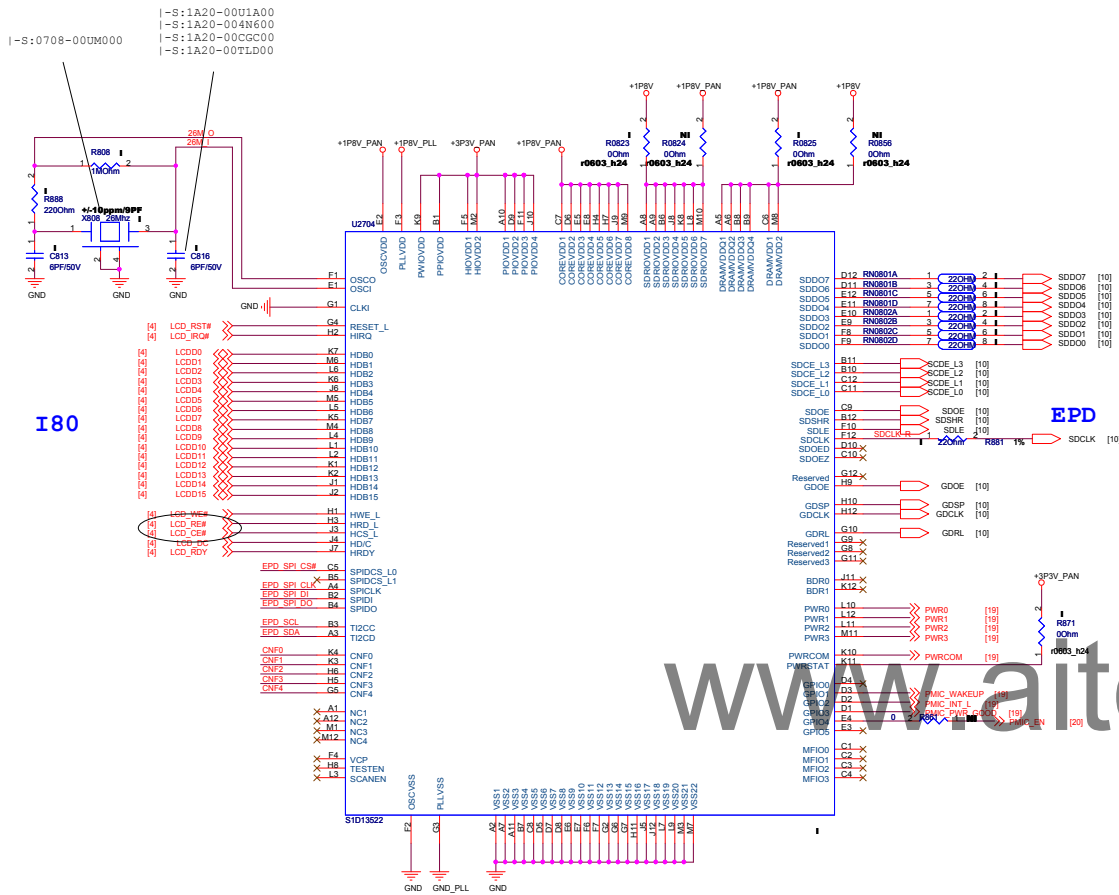
Change to 0500-00TV000

0-00TV000

~~MOVINAND DERUG SW ( SD )~~

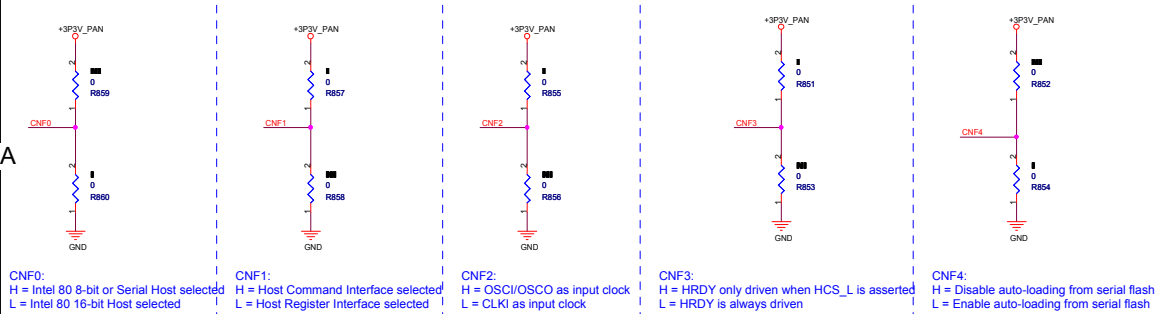


## EINK CONTROLLER 13522

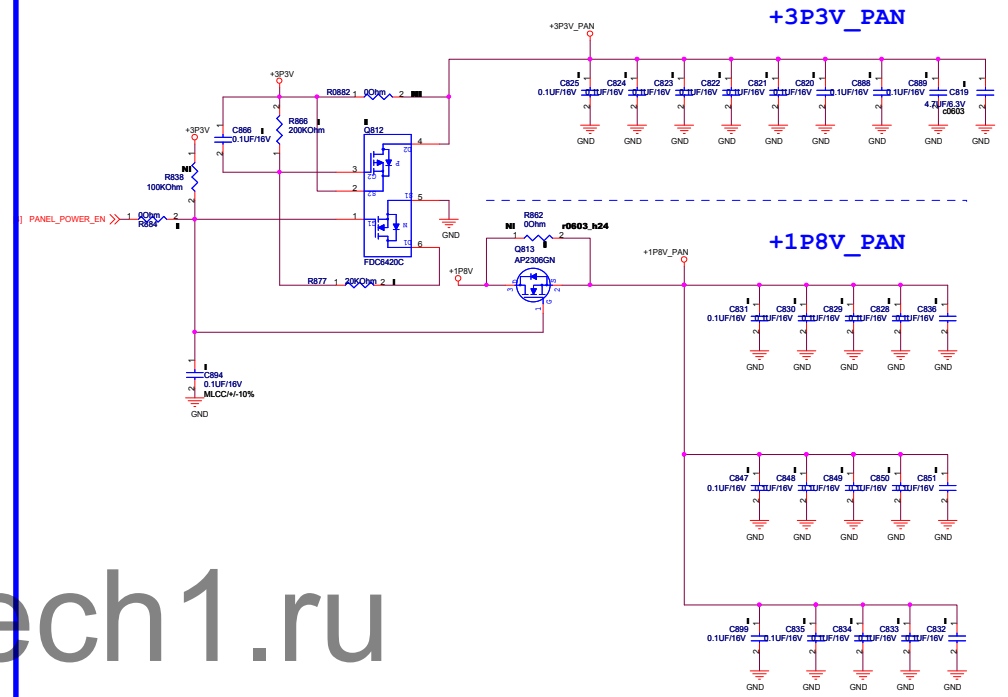


**nb\_bga\_144p\_31\_394x394\_h47**

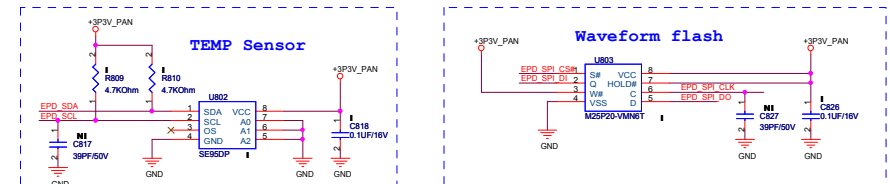
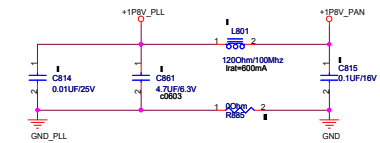
## Strapping



PANEL PWR SAVE ( +3P3V\_PAN & +1P8V\_PAN )



## +1P8V\_PLL



<Variant Name>

**PEGATRON DT-MB RESTRICTED SECRET**

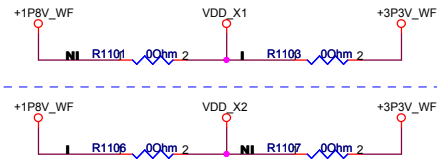
<b>PEGATRON</b>		Title : <b>EINK(1) - Controller</b>	
PEGATRON CORP.		Engineer: <b>Stanley921_Lee</b>	
Size <b>A2</b>	Project Name <b>EB-306</b>		Rev <b>1.03</b>
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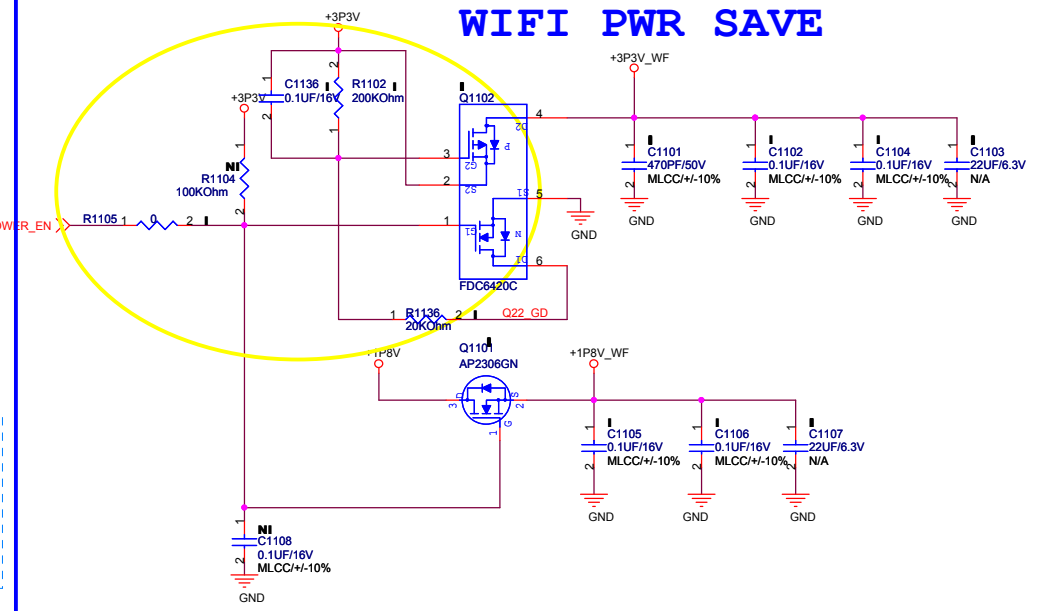
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## I/O VOLTAGE



## WIFI PWR SAVE



## 1.2V SEL GND Internal LDO Floating External

NOBOM 00hm R1108

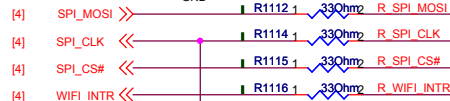
## HOST Interface SEL R R SPI N N SDIO

700mW

## RF Trace 50

Ohm

## ECSn N SPI R SDIO



For EMI

[14] 32.768K

Pin 5,6,8,28,30,34 JTAG pin internal use , let floating

PEGATRON DT-MB RESTRICTED SECRET

<Variant Name>

PEGATRON Title : WIFI

PEGATRON CORP. Engineer: Stanley921\_Lee

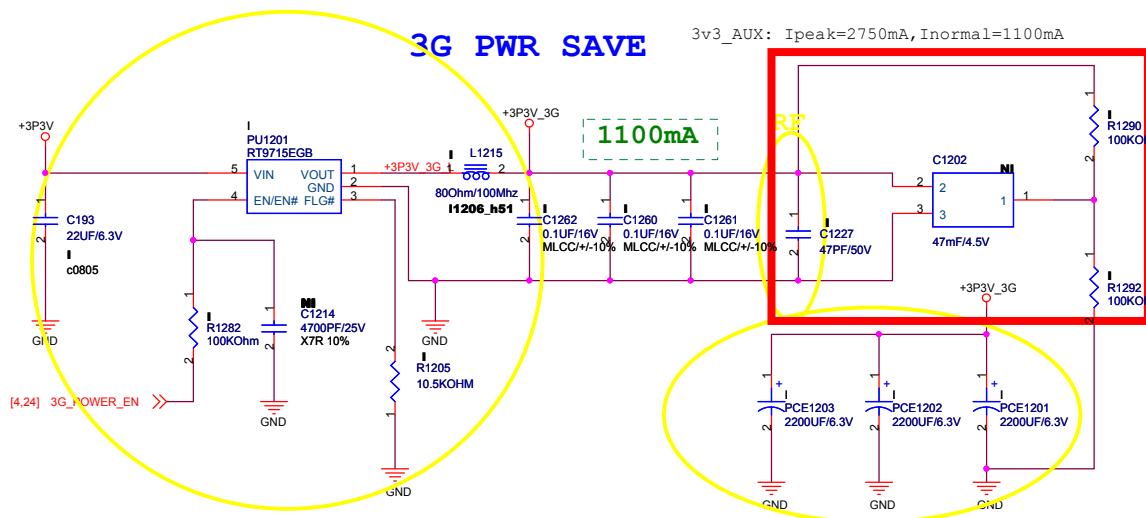
Size A3 Project Name EB-306

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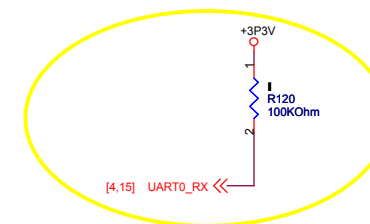
Rev 1.03

## 3G PWR SAVE

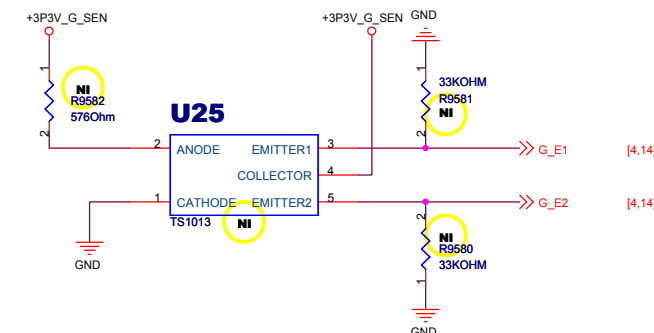
3v3\_AUX: Ipeak=2750mA, Inormal=1100mA



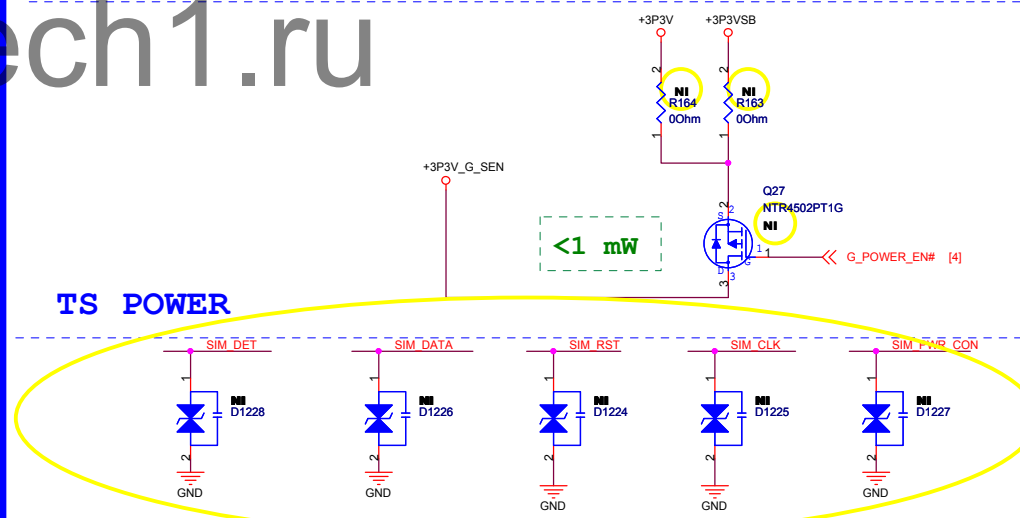
## Factory testing



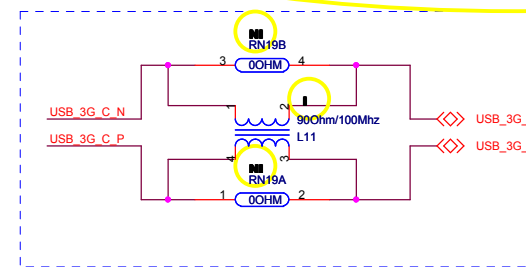
## TS1013



## TS POWER



## PROTECT DIODE



PEGATRON DT-MB RESTRICTED SECRET

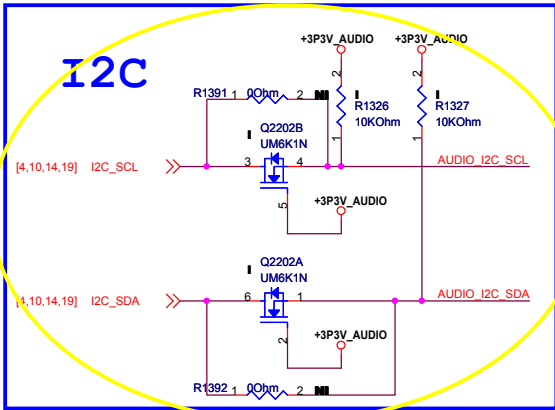
PEGATRON Title : 3G (EM770) & Rotation

PEGATRON CORP. Engineer: Stanley921\_Lee

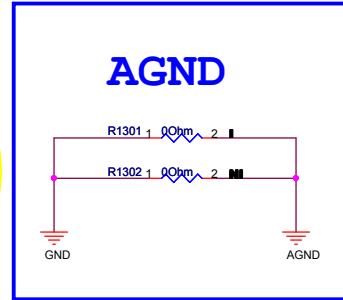
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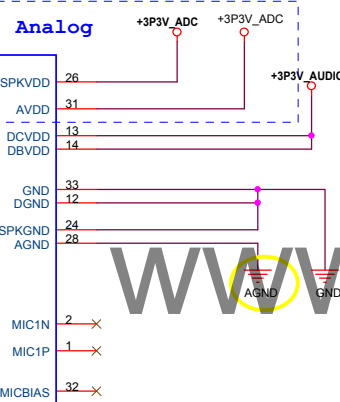
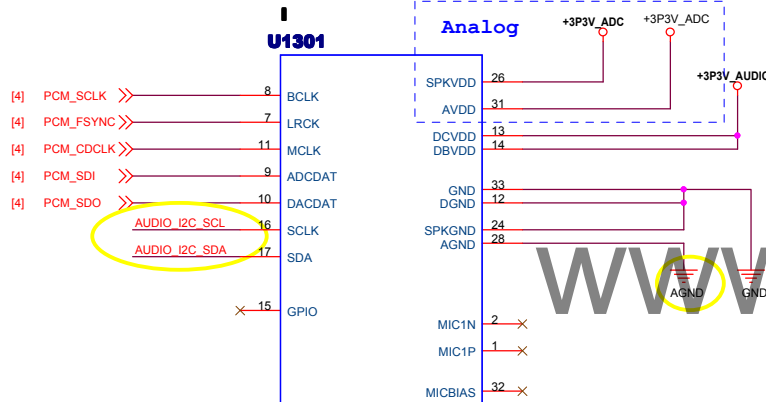
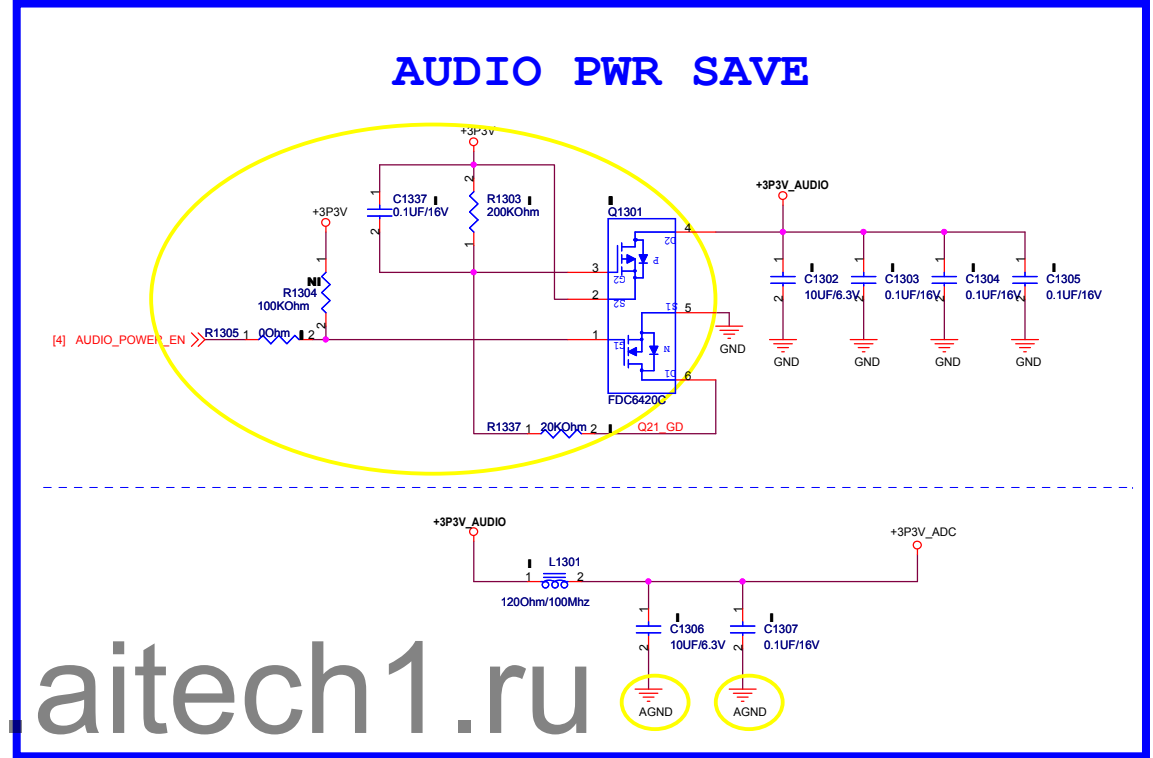
12C



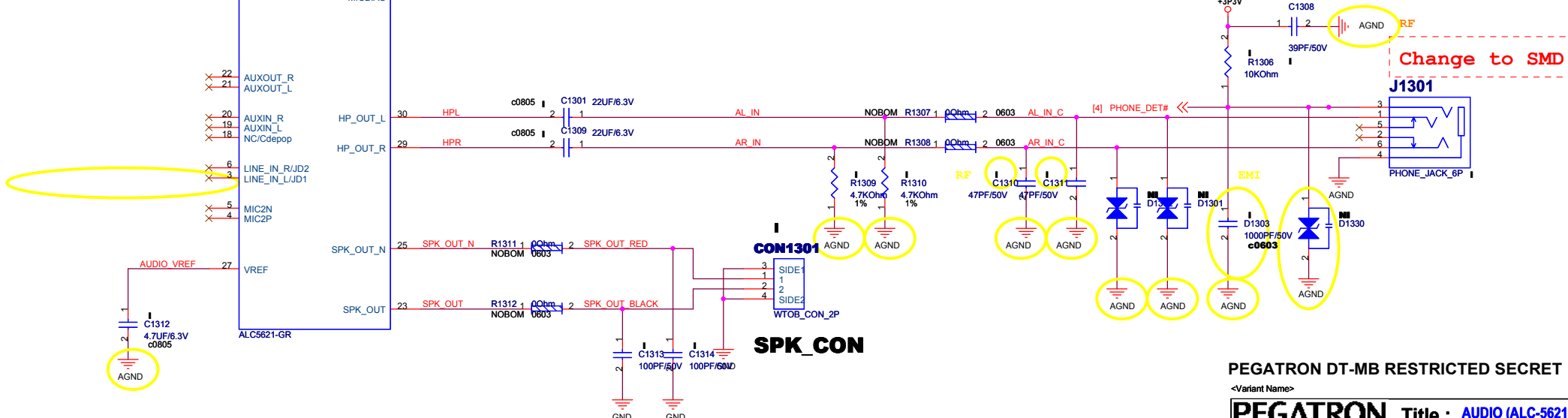
AGND



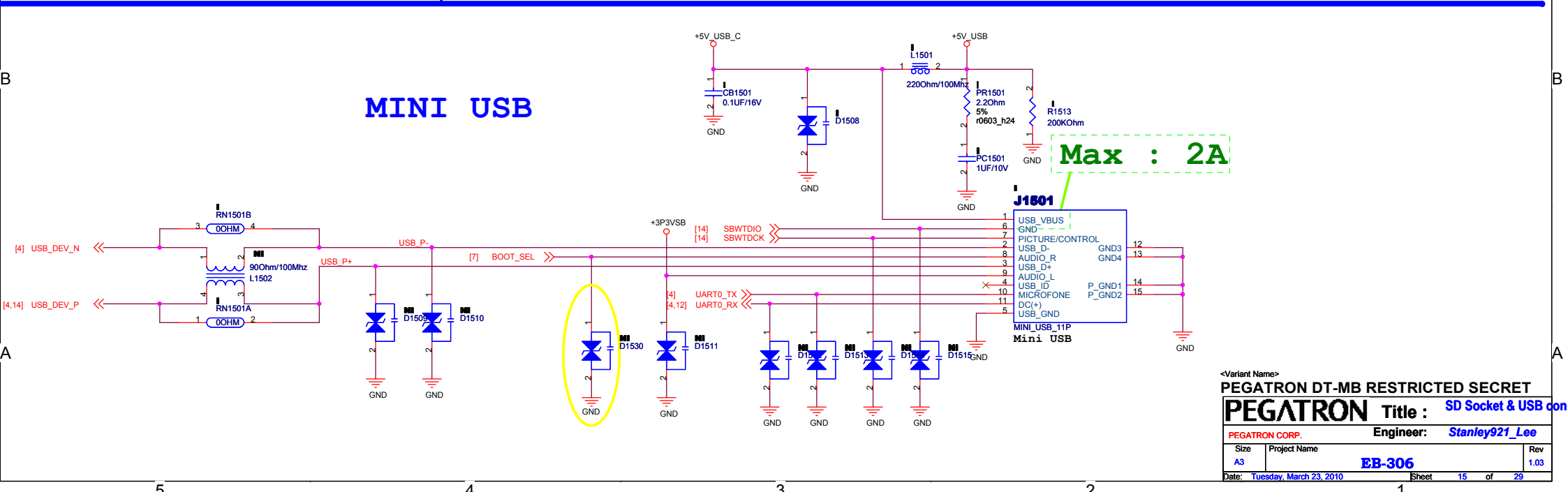
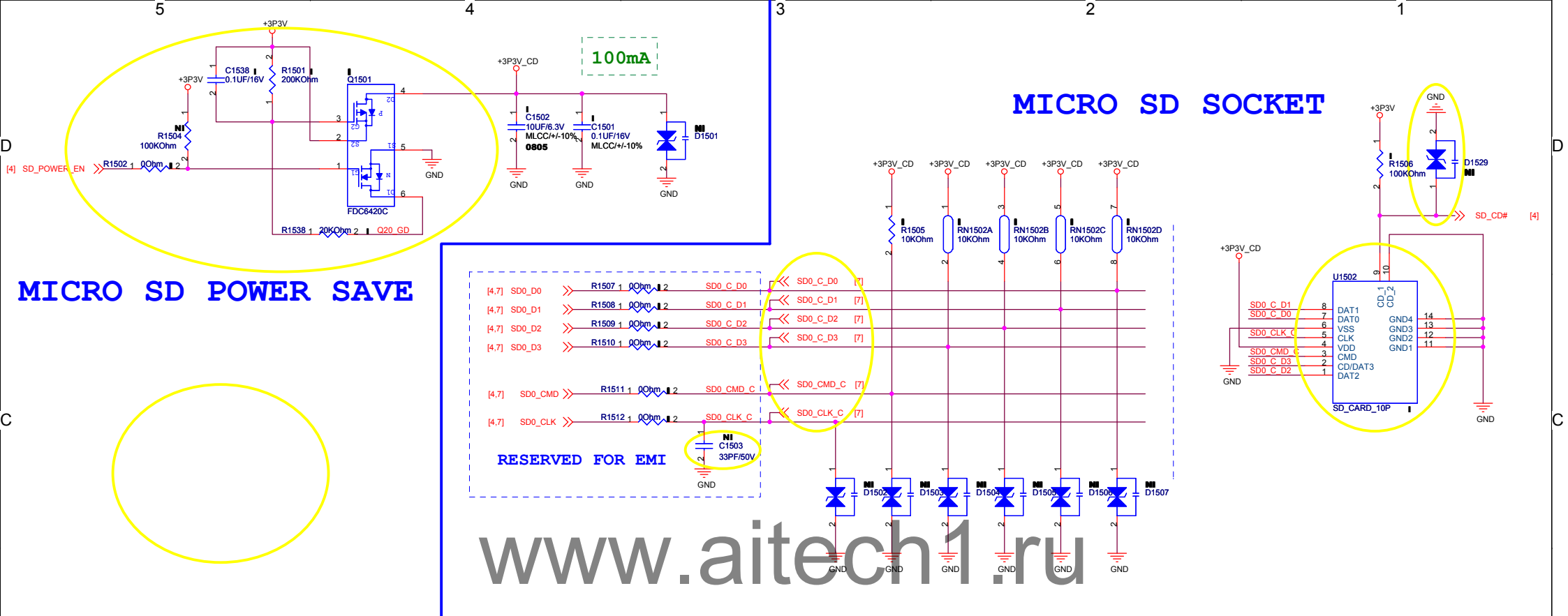
AUDIO PWR SAVE

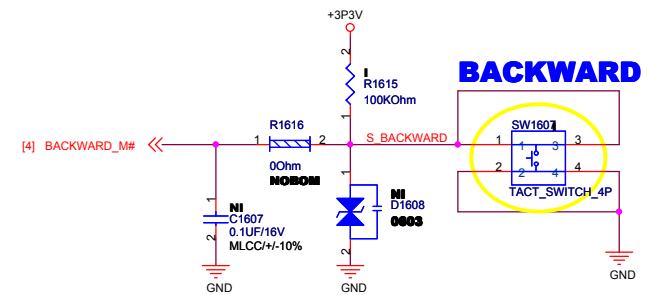
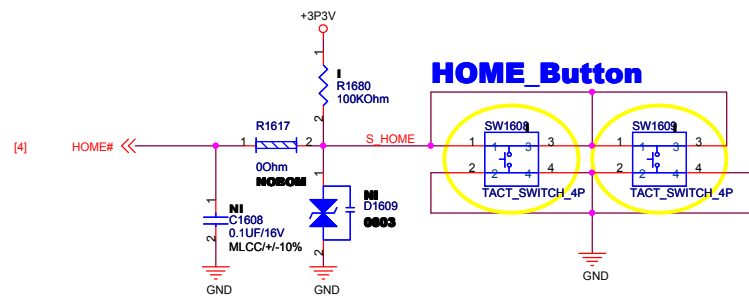
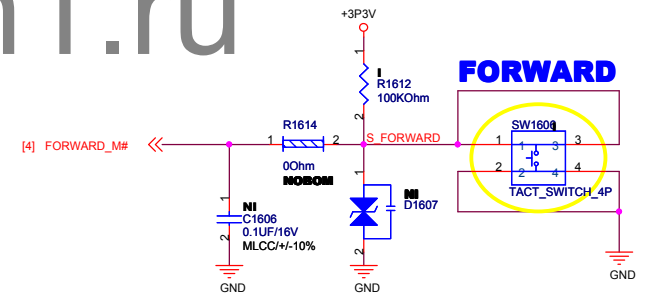
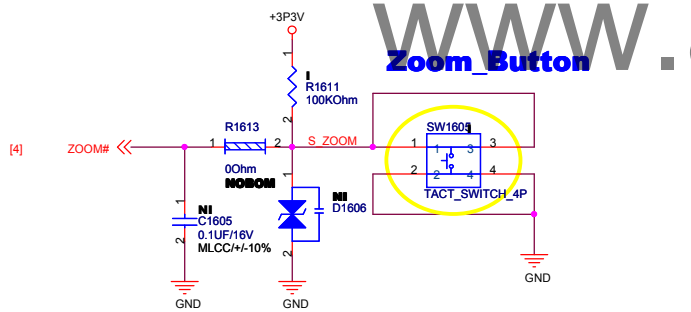
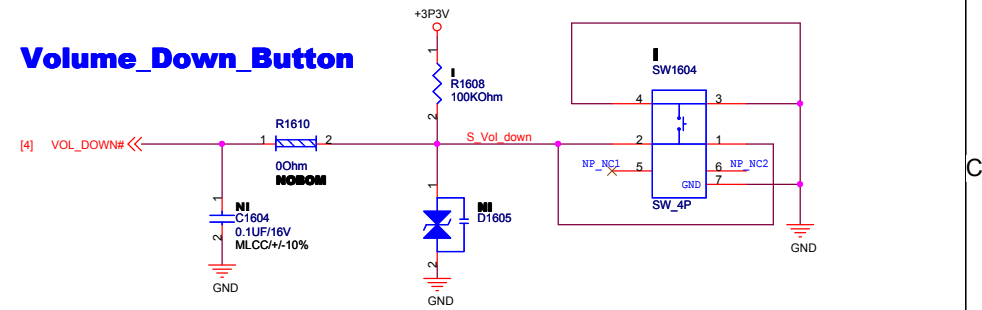
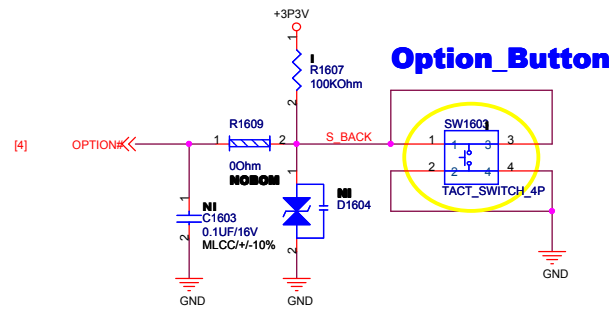
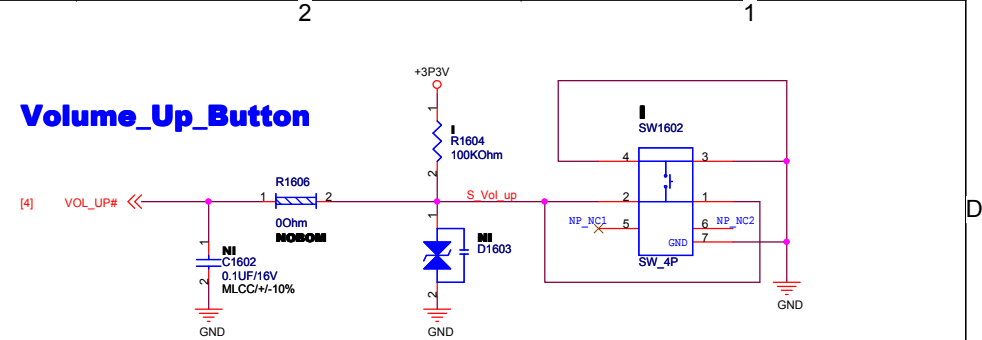
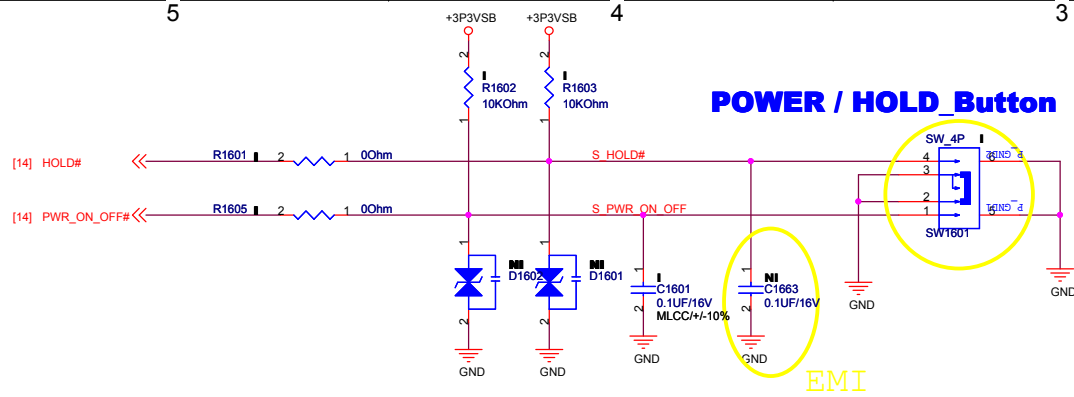


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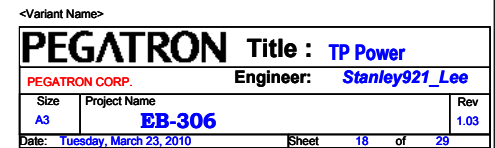






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2nd Source  
 0703-000B000 SCHOTTKY FS1J4TP  
 0703-001T000 SCHOTTKY PMEG4010EH  
 0703-00AW000 SCHOTTKY SS1040FL  
 0703-001C000 SCHOTTKY 1N5819HW-7-F

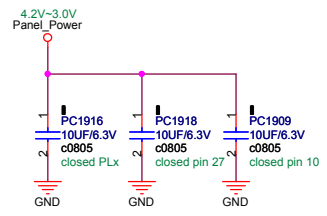
change component 4.7uH => 2.4uH  
 PL1902 1 2 2.4uH  
 FS1J4TP 40V/1A

# TPS65181

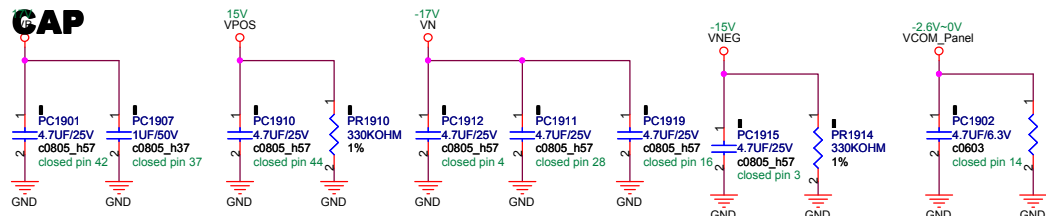
change component 4.7uH => 2.4uH  
 PL1901 1 2 4.7uH  
 FS1J4TP 40V/1A

**Change to 0664-001Y000**

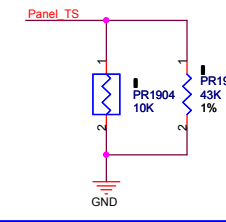
## Input CAP



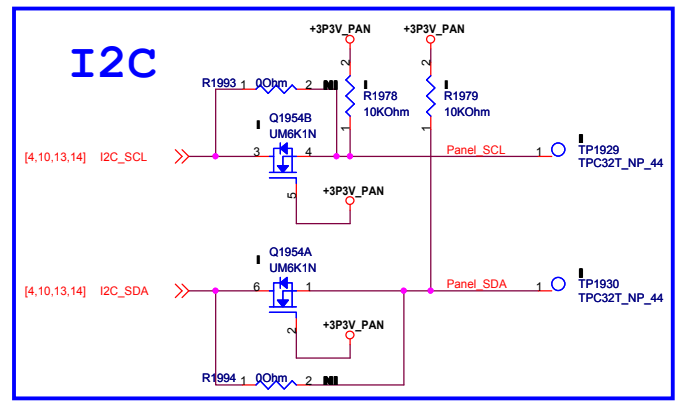
## Output CAP



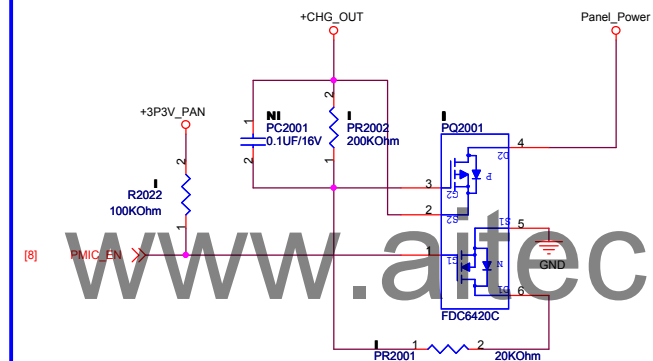
## Thermal



## I2C



## VIN POWER OF 65181



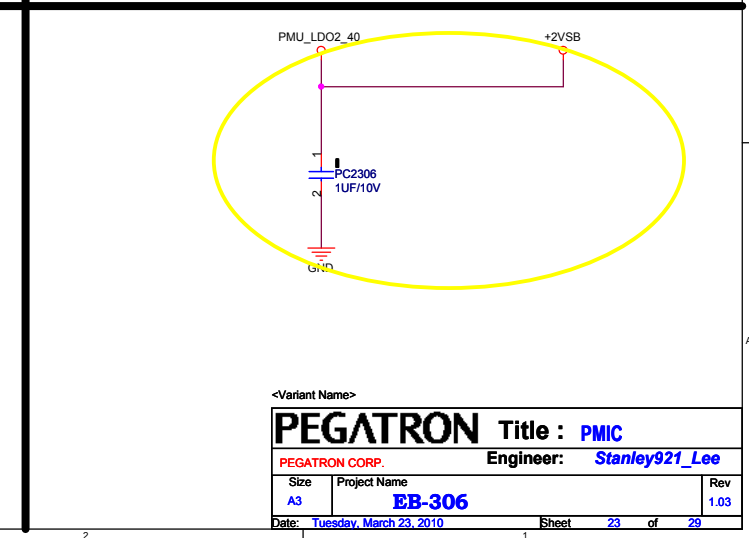
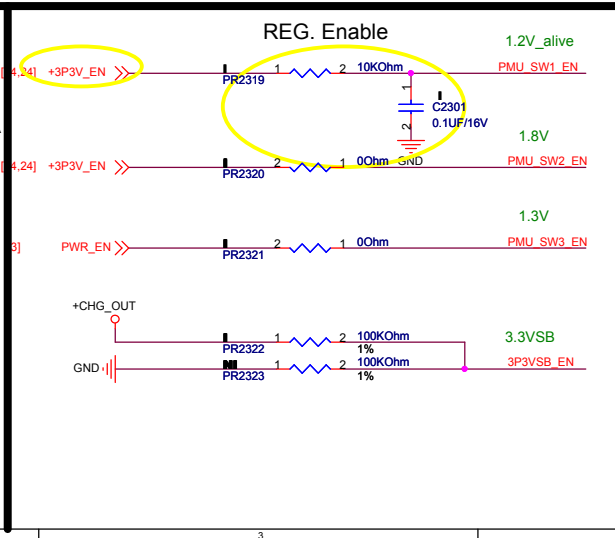
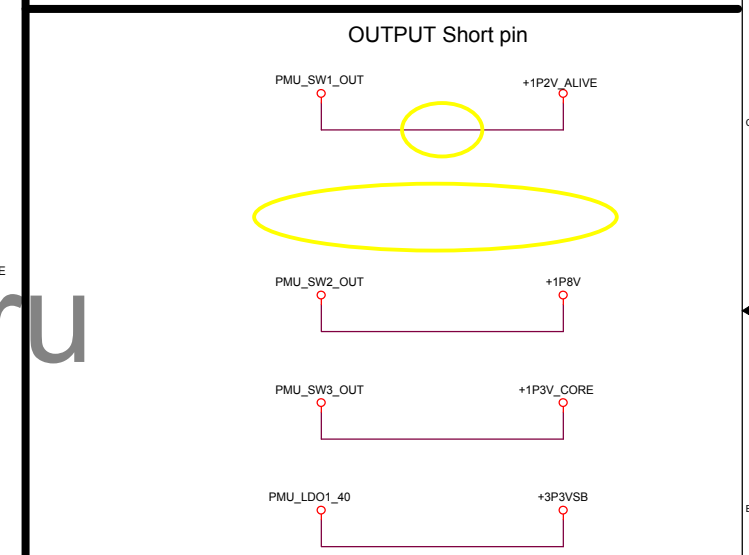
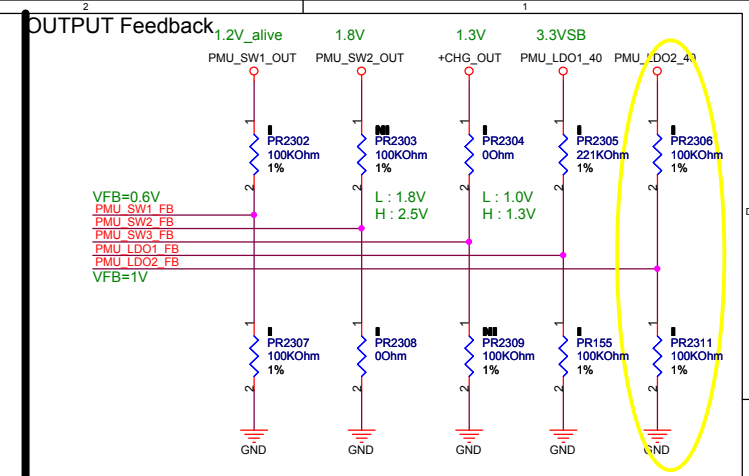
<Variant Name>

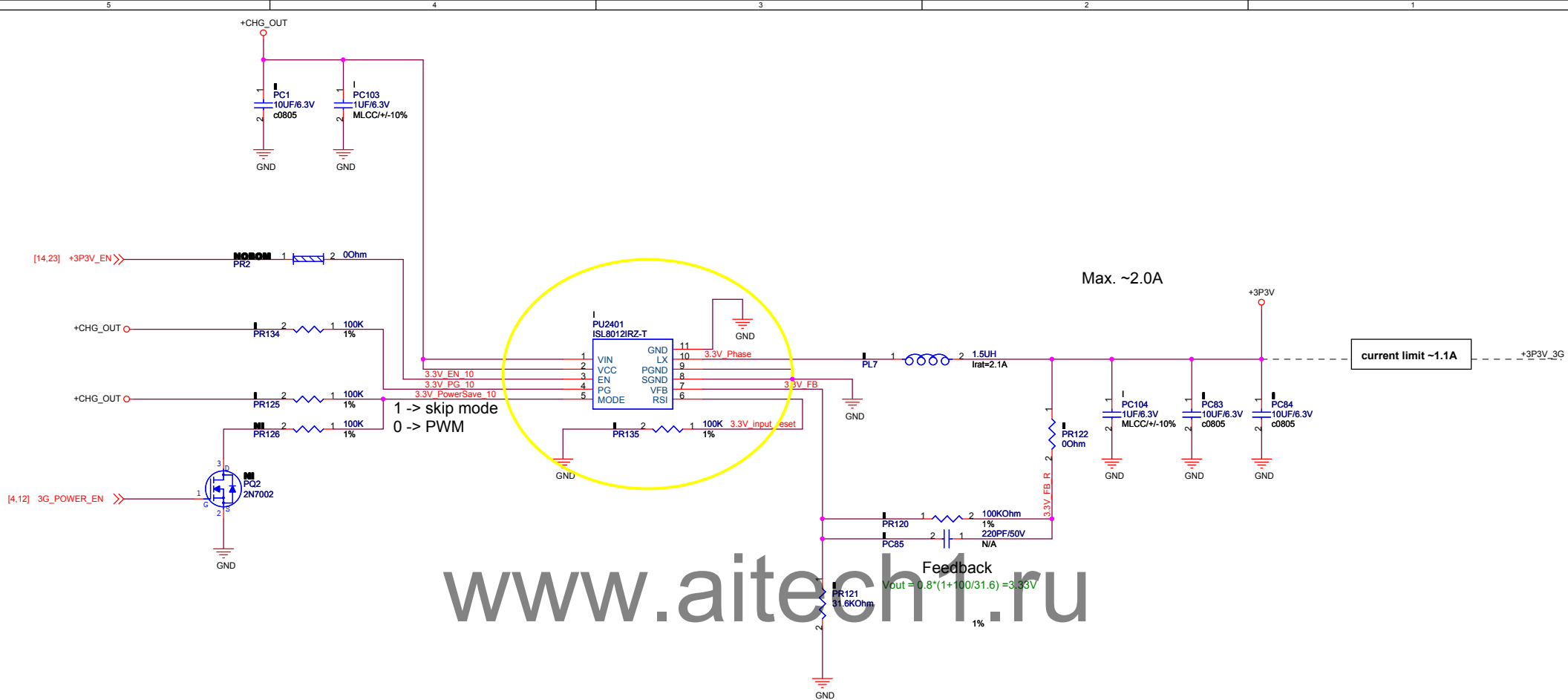
<b>PEGATRON</b>		Title :	Panel Power (VDDH_VEE)
PEGATRON CORP.		Engineer:	Stanley921_Lee
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A3	N620	1.03	
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<b>PEGATRON</b>		Title : <b>Panel Power (VCOM)</b>	
PEGATRON CORP.		Engineer: <b>Stanley921_Lee</b>	
Size	Project Name		Rev
A3	<b>K1</b>		1.03
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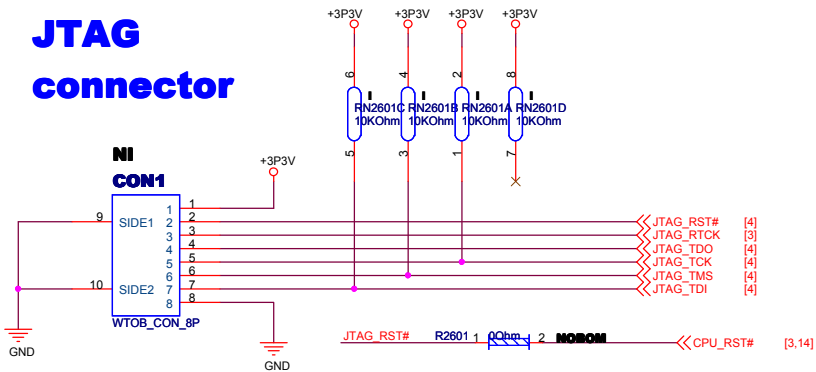
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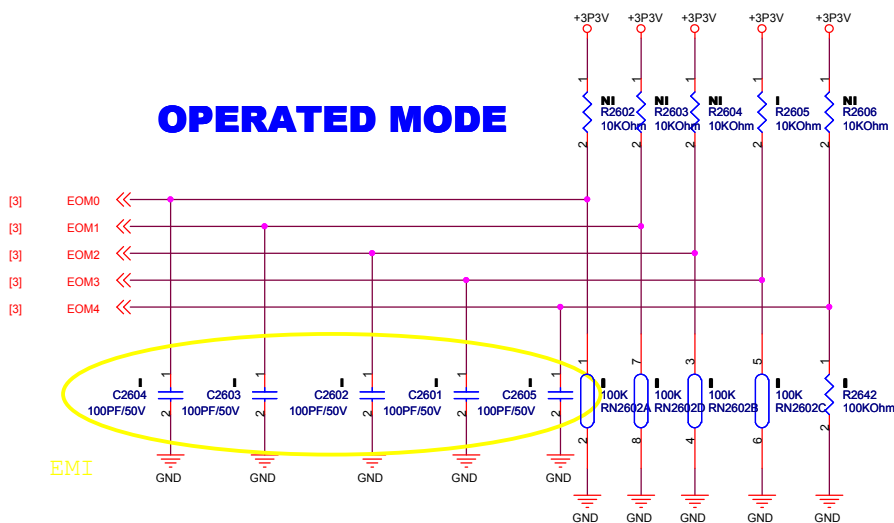
Title			
<Title>			
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## JTAG connector

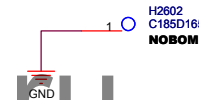


OM[4]	OM[3]	OM[2]	OM[1]	OM[0]	OM[4]	OM[3]	OM[2]	OM[1]	OM[0]	Operation Mode
0	0	0	0	0	NAND	Large Block	Page (4K)	Add (4)	OSC	NAND
			1	EXT						
			0	OSC						
			1	EXT						
		1	0	0			Page (2K)	Add (4)	OSC	
			1	EXT						
			0	OSC						
			1	EXT						
	1	0	0	0	1ROM	OSC	1ROM			
						EXT				
1	0	0	0	Reserved				Reserved		
			1	JTAG				JTAG		

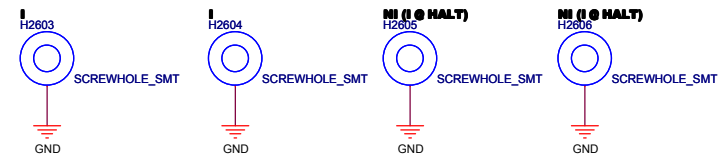
## OPERATED MODE



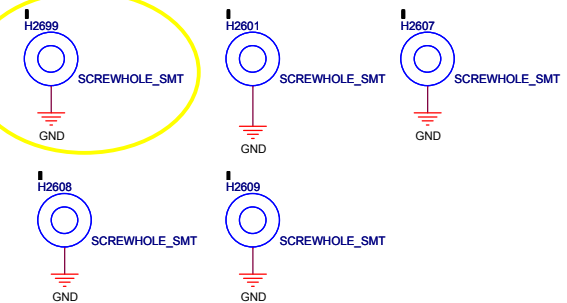
## HOLE

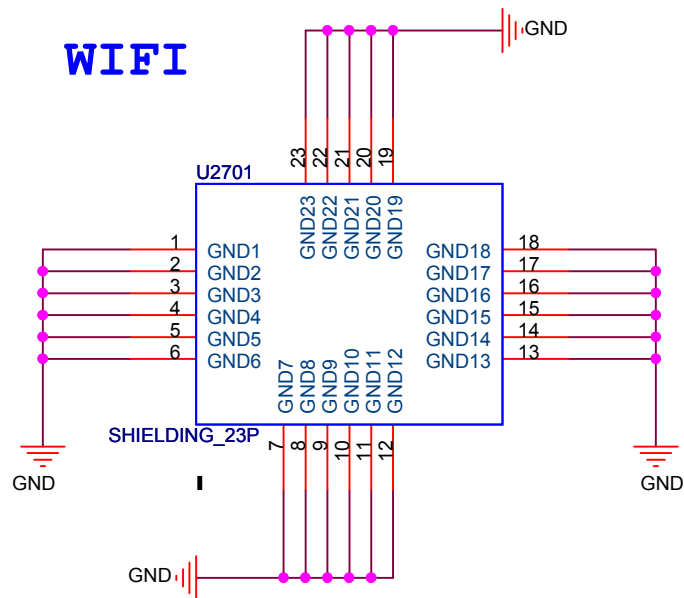


## 3G NUT



## ME NUT

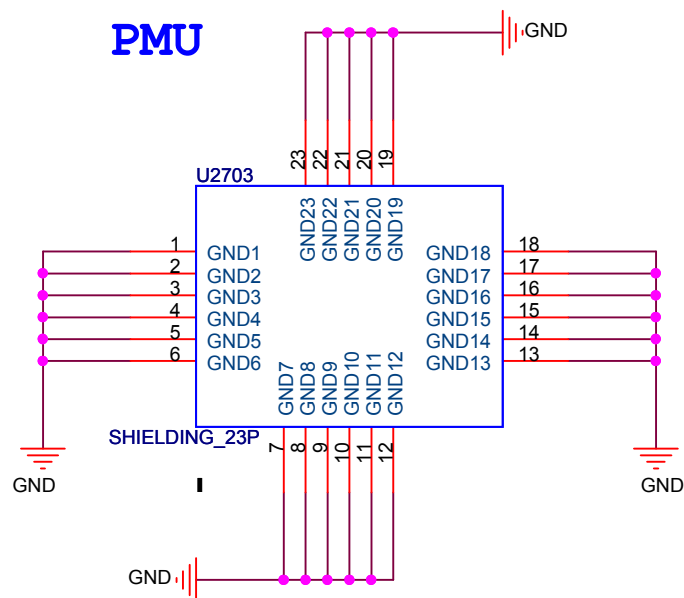




**Eink**

**CPU**

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<b>PEGATRON</b>		Title : <b>Shielding Frames</b>	
PEGATRON CORP.		Engineer: <b>Stanley921_Lee</b>	
Size <b>A</b>	Project Name <b>EB-306</b>		Rev <b>1.03</b>
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CUBIC Version Change Table

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